

**Assembly Relocation to STATS ChipPAC Jiangyin and
Test Transfer to STATS ChipPAC Singapore of Select LFCSP Products**

**Qualification Results Summary for
LFCSP at STATS ChipPAC China Jiangyin**

TEST	SPECIFICATION	SAMPLE SIZE	RESULTS
Temperature Cycle (TC)*	JEDEC <i>JESD22-A104</i>	3 x 77	PASS
Solder Heat Resistance (SHR)*	JEDEC/IPC <i>J-STD-020</i>	3 x 11	PASS
Unbiased Highly Accelerated Stress Test (uHAST)*	JEDEC <i>JESD22-A118</i>	3 x 77	PASS
Electrostatic Discharge <i>Field Induced Charge Device Model</i>	JEDEC <i>JESD22-C101</i>	3/voltage	PASS ±750V

* Preconditioned per JEDEC/IPC J-STD0020